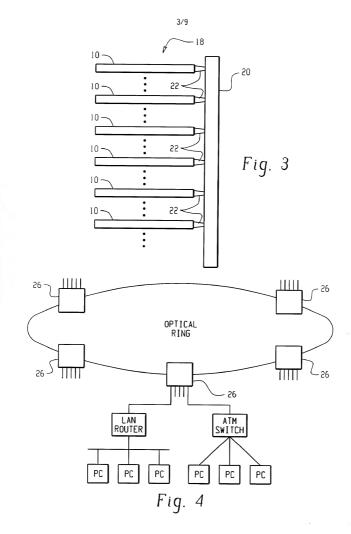
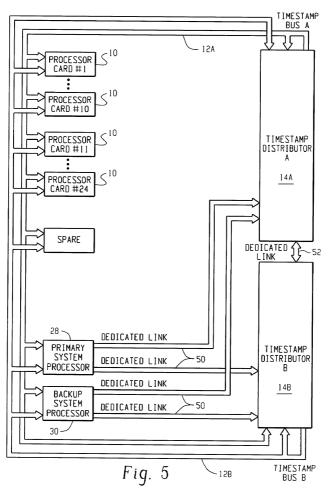


Fig. 2





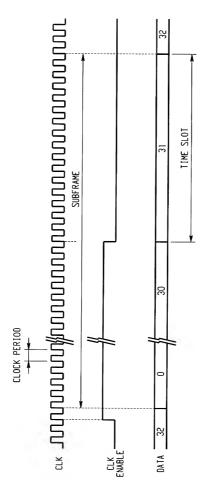


Fig. 6

PRIMARY SYSTEM PROCESSOR BACKUP SYSTEM PROCESSOR

TIMESTAMP DISTRIBUTOR A

ASTRIBULE TIME VALUE

TIMESTAMP DISTRIBUTOR 6 TIME VALUE A

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**ASSIGNMENT** 

SPARE CARO

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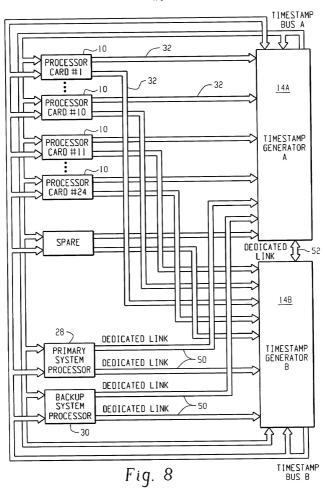
	E SI	24	23	56	27	82	ද	೫	3
	NO BITS	32	32	32	32	32	32	32	32
	ASSIGNMENT	CARD 17	CARD 18	CARD 19	CARD 20	CARD 21	CARD 22	CAR023	CARD24
	TIME SL0T	91	11	81	61	20	17	22	53
	NO BITS	32	32	32	32	32	32	32	32
	ASSIGNMENT	CARD 9	CARD 10	CARD 11	CARD 12	CARD 13	CARD 14	CARD 15	CARD 16
	TIME SLOT	8	9	10	11	12	13	14	15
	NO BITS	32	32	32	32	32	32	32	32
	ASSIGNMENT	CARD 1	CARD 2	CARD 3	CARD 4	CARD 5	CARD 6	CARD 7	CARD 8
	TIME SLOT	0	-	2	3	4	5	9	7

NO BITS 207

TIME ASSIGNMENT

UNASS I GNE D

35



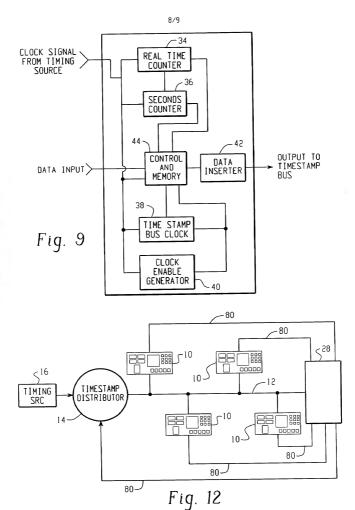


Fig. 11